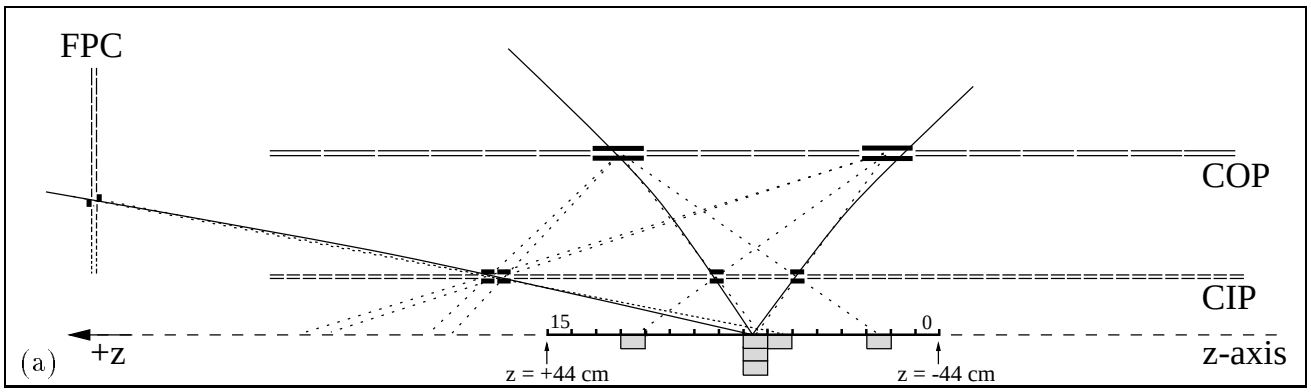
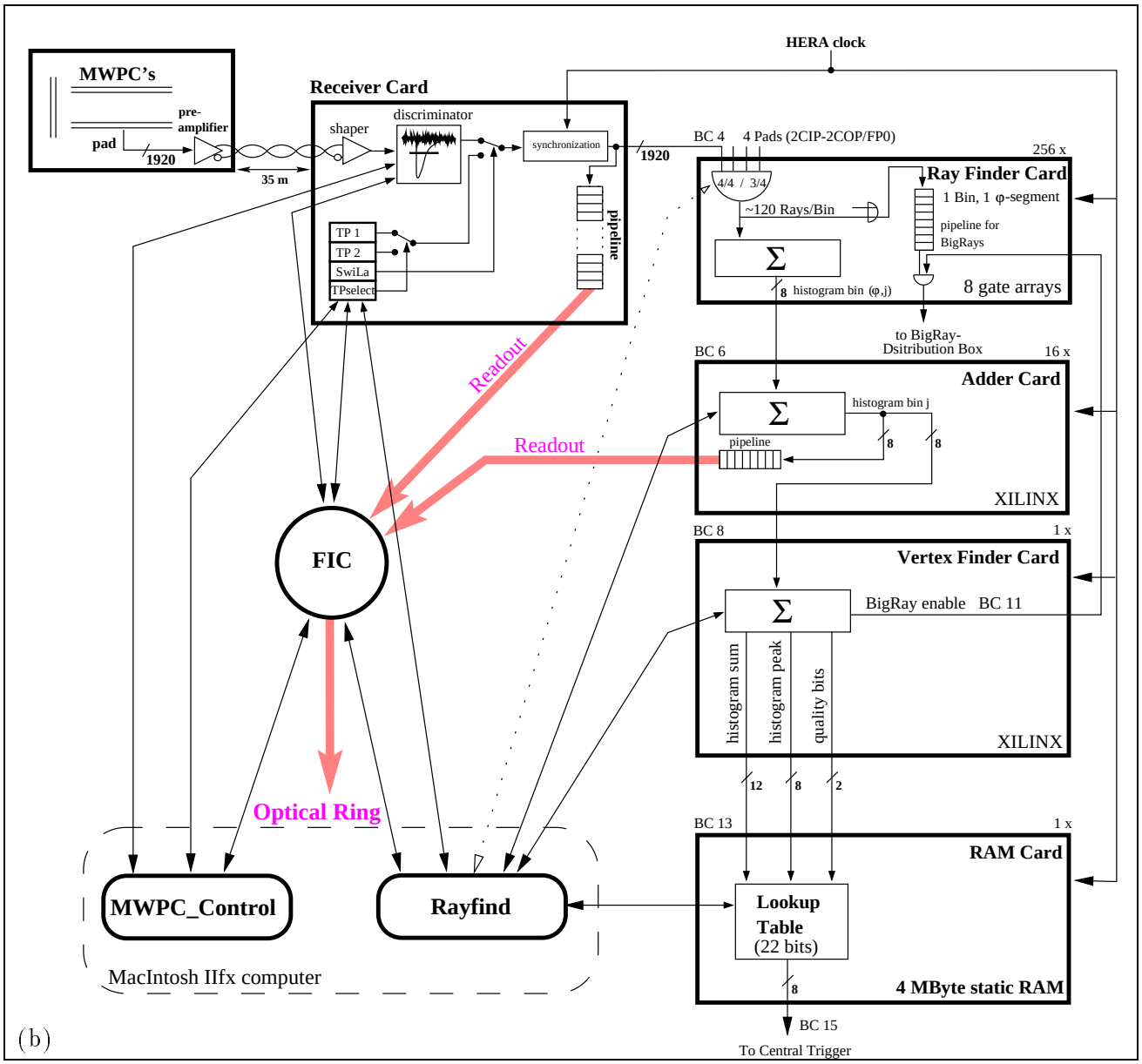

FPC
COP
CIP
15
0
(a)
+z
z = +44 cm
z = -44 cm
z-axis


HERA clock
MWPC's
pre-
amplifier
pad
1920
35 m
Receiver Card
shaper
discriminator
synchronization
1920
TP 1
TP 2
SwiLa
TPselect
pipeline
BC 4
4 Pads (2CIP-2COP/FP0)
256 x
Ray Finder Card
4/4 / 3/4
~120 Rays/Bin
1 Bin, 1 φ-segment
pipeline for
BigRays
Σ
8
histogram bin (φ,j)
8 gate arrays
to BigRay-
Dsitribution Box
BC 6
16 x
Adder Card
Σ
histogram bin j
pipeline
8
8
XILINX
Readout
Readout
FIC
BC 8
1 x
Vertex Finder Card
Σ
BigRay enable BC 11
histogram sum
histogram peak
quality bits
XILINX
12
8
2
Optical Ring
BC 13
1 x
RAM Card
MWPC_Control
Rayfind
Lookup
Table
(22 bits)
8
4 MByte static RAM
MacIntosh IIfx computer
BC 15
To Central Trigger
(b)