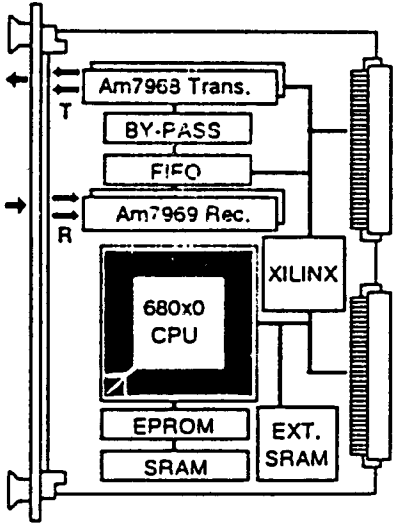
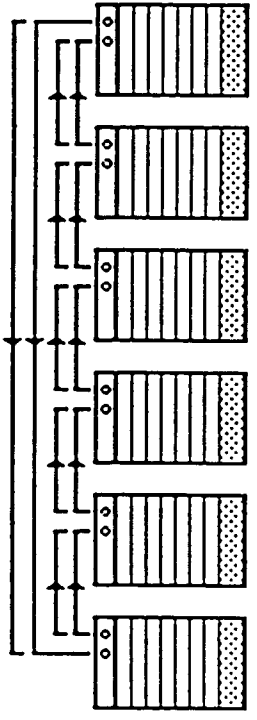


The optic-fibre ring loops around all crates. Data sent out by one taxi transmitter are received at the next VMEtaxi in the ring. Direct connections can be enabled between any two devices using By-pass registers.

The upgraded Mark-2 boards are able to exploit 50 MHz 68030 microprocessors and 250 MHz taxi chips working in parallel with double links.



The upgraded version uses XILINX gate arrays for VME64 and VSB block transfers. Separate program memory and up to 2 MBytes of dual-ported extension ram is also provided so as to fully optimise the data block-moving performance to 50 MBytes/sec.